

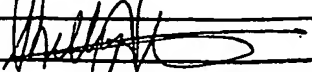
FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ISTOR.012A	APPLICATION NO. 10/781,553
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT Wang et al.	
		FILING DATE February 17, 2004	GROUP 2881 2112

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
SAC	1.	Broadcom, BCM5706 Product Brief © 2004 by Broadcom Corporation
SAC	2.	End System Optimizations for High-Speed TCP, IEEE Communications Magazine, April 2001
SAC	3.	Meet the Engineers Networking Reception, Intel Developer Forum, © 2002 Intel Corporation
SAC	4.	Alacritech® SES1001 iSCSI Accelerator, iSCSI Controller for Microsoft iSCSI Software Initiator, © Alacritech 2004
SAC	5.	Silverback iSNAP™ 2100, IP Storage Network Access Processor, © 2002 Silverback Systems
SAC	6.	Silverback iSNAP™ 2110, IP Storage Network Access Processor, © 2004 Silverback Systems
SAC	7.	iSCSI Controller, 1-Gbps iSCSI to 133-MHz PC1-X Controller, ISP4010, © 2003 QLogic Corporation
SAC	8.	Aristos Logic, Intelligent Storage Processors, A Powerful Building Block for RAID and Virtualization Systems, 2002
SAC	9.	The Linley Group, A guide to Storage Networking Silicon, Second Edition, Published December 2003, © 2003 The Linley Group, Inc.
SAC	10.	IBM PowerPC 440GX Embedded Processor Data Sheet; 6/30/04
SAC	11.	10 GEA, Introduction to TCP/IP Offload Engine (TOE), Version 1.0, April 2002

H:\DOCS\TMCTMC-2234.DOC
092404

EXAMINER 	DATE CONSIDERED 5-1-07 / 2-4-08
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	